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Viktor Valouch (IEEE LSM'21) received the M.S. and Ph.D. degrees in electrical engineering from the Czech Technical University in Prague, Czech Republic, in 1972, 1976, respectively. From 1975 to 1977 he was a researcher with CKD Semiconductors, Prague, being engaged in the development of power converters. In 1977 he joined the Department of Electrical Engineering and Electrophysics, Institute of Thermomechanics, Czech Academy of Sciences as a principal researcher. Since 2001, he has been a Professor at Faculty of Electrical Engineering, Czech Technical University in Prague. He has been a leader of many R&D projects in the field of industrial electronics. His research interests include pulse width modulation (PWM) strategies, power theories, and control strategies for grid-connected converters.

Additional Information:

- Member of the ICREPQ-International Scientific Committee.
- RE&PQJ-Scientific Committee Member