Considerations regarding testing of low voltage equipment at electrical arc and thermal factor actions

Dr. Eng. George CURCANU, Eng. Constantin IANCU, Phys. Daniel TRUTA

R&D National Institute ICMET-Craiova, 144 Calea Bucuresti, 200515 Craiova, Romania
phone: +40351 402427; +40351 404 888; +40351 404 889
fax: +40351 404 890; +40251 415 482
http://www.icmet.ro, email: LMP@icmet.ro

Abstract: Within paper there is made a synthesis of IEC standards requirements for testing at overload and short-circuit currents of low voltage equipment: switches, disconnectors, switch- disconnectors, contactors, fuses and fuses combinations with these. After this there is presented testing circuit for withstand ability at overload and short-circuit currents passing of low voltage equipment for a large range of currents through realisation of a special circuit for load adjustment. Also are described experiments performed on low voltage fuses - as type tests - and are established appreciation criteria regarding the behaviour at electric arc action due to overload and short-circuit currents.

Keywords: low voltage equipment, tests, standards, data acquisition and processing

1. Introduction

In order to certify the quality of products at sectorial level within reglemented domain, according to European Directive 73/23 CEE for free circulation on international market of products marked CE, low voltage equipment must be tested at electric arc and thermal factor action due to overload and short-circuit currents passing. In this aim must be performed tests according to actual IEC standards within specialised laboratories, endowed with corresponding equipment for achieving the test circuits. In order to establish the test circuit we must analyse the requirements of IEC test standards [1] specifically for low voltage apparatus and equipment.

2. Requirements of testing standards

In order to appreciate the withstand ability of low voltage equipment at overload and short-circuit currents passing there are performed type tests according to IEC standards [1]. The IEC requirements establish that tests to be performed with five test duties in a certain order and at specified tests parameters (voltages, currents, power factors) [1,2]. Testing standards establish: number of tests to be performed, nominal duty cycles (for switches apparatus), testing circuits, appreciation criteria of test behaviour and apparatus status after test.

Testing of low voltage equipment and apparatus are very complex and they seek for verification of following operating capability:
- in service at rated currents (temperature-rise test)
- at short-circuit currents (making and breaking test, short-time and peak current withstand test, short-circuit test etc)
- overload currents test [1][2]

As a synthesis of testing standards requirements [1][2] we present test duties for commutation apparatus in Table 1 and for fuses in Table 2.

Table 1 - General scheme of test duties for low voltage switchgears and controlgears

<table>
<thead>
<tr>
<th>Test sequence</th>
<th>Test</th>
<th>Cycles</th>
</tr>
</thead>
<tbody>
<tr>
<td>I – General performance characteristics</td>
<td>- Overload performance. - Temperature-rise - Overload releases</td>
<td></td>
</tr>
<tr>
<td>II – Rated service short-circuit breaking capacity</td>
<td>- Rated service short-circuit breaking capacity. - Temperature-rise - Overload releases</td>
<td>O-t-CO-t-CO</td>
</tr>
<tr>
<td>IV – Rated short-time withstand current</td>
<td>- Overload releases - Rated short-time withstand current - Temperature-rise - Short-circuit breaking capacity at maximum short-time withstand current - Overload releases</td>
<td>O-t-CO</td>
</tr>
<tr>
<td>V – Performance of integrally fused circuit-breaker</td>
<td>Stage 1 - Short-circuit at selectivity limit current - Temperature-rise</td>
<td></td>
</tr>
</tbody>
</table>
### Table 2 - General scheme of test duties for low voltage fuses

<table>
<thead>
<tr>
<th>Test duty / Type</th>
<th>Parameters</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Ur</td>
</tr>
<tr>
<td>Test duty 1</td>
<td>110^15 U₀</td>
</tr>
<tr>
<td></td>
<td></td>
</tr>
<tr>
<td>Test duty 2</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
</tr>
<tr>
<td>Test duty 3</td>
<td></td>
</tr>
<tr>
<td>Test duty 4</td>
<td></td>
</tr>
<tr>
<td>Test duty 5</td>
<td></td>
</tr>
</tbody>
</table>

Un – rated voltage
Ur - recovery voltage
I₇ - test current
I₀ - rated breaking current
I₄ - breaking current for maximum energy
I₃ - overload currents
I₅ - fuse melting current
I₆ - fuse rated current

### 3. Testing circuit

Testing circuit for verification of the withstand ability of low voltage equipment at overload and short-circuit currents passing are presented in Figure 1.

---

**Figure 1 - Testing circuit scheme**

- **Network 6 kV**
  - S₁: CB₁, T₁: CB₁, S₁
  - S₂: CB₂, T₂: CB₂, S₂
  - S₃: CB₃, S₃
  - S₄: CB₄, S₄
- **Circuit I (C₁)**
  - T₁₅: Transformer 15 MVA, 10,5 / 6,3 kV
  - T₁, T₂: Transformers 1000 kVA, 6 kV/0,4 kV
  - G: Generator 2500 MVA, 12 kV
  - S₆₅, S₆₆: Disconnectors 6 kV / 1250 A
  - CB₁, CB₂: Circuit-breakers 12 kV / 1250 A
  - CB₃, CB₄: Circuit-breakers 500 V / 2000 A
  - S₆₇, S₆₈: Disconnectors 0,4 kV / 1250 A
  - CB₅: Circuit-breaker 6 kV / 2500 A
  - S₆: Making switch 6 kV / 2500 A
  - Xₐ: Reactances 100 mΩ / 10 kA – 0,055 – 1,8 Ω / phase
  - Xₚ: Reactances 3 - 550 Ω / phase

---

Note: Rated breaking capacity at short-circuit, short-time maximal rated current are established by manufacturer. Also it specifies, for circuit-breakers and contactors, the utilisation category and, for fuses, constructive type.
According to Figure 1, testing circuit is composed of:
- high current and voltage supply circuit (C1)
- load adjustment circuit (C2)
- measuring and recording circuit (C3)

As voltage supply are, depending of test performed, 6 kV network, 15 MVA network transformer, 1000 kVA power with step down ration 6 kV/1.0/0.5/0.25 kV or 2500 MVA short-circuit generator with variable voltage (1÷12 kV).

As impedance load, within load adjustment circuit, are used: resistors batteries (0.05 Ω - 125 Ω) and reactors batteries (0.25 mH - 250 mH). Resistors and reactors are series connected in circuit and in parallel are connected the capacitors and resistors to adjust the parameters of transient recovery voltage. In order to can perform tests in large range of currents (4 A - 7500 A) at voltage of 380 V; 660 V; 1000 V, there were determined, based on calculus, reactances and resistances values required for tests duties 3,4 and 5 (power factor: cosφ = 0.3 - 0.5).

For adjustment of load current according to test standards, resistors and reactors batteries were realised with no inductive resistors and inductance coils without iron core, in modular construction, with the values of resistances and inductances in geometrical progression with ratio 2. [3]

Resistors battery consists of four modules on each phase, endowed with adjust terminals, and reactors battery consists of inductance coils in single- and multilayer construction, having the values of: 0.25 mH; 0.5 mH; 1 mH; 2 mH; 4 mH; 8 mH and 16 mH. The arrangement of coils modules is rectangular symmetrical (90°) to avoid coupling inductances at overload currents passing.

Measuring and recording circuit consists of:
- current transducers: Rogowski coils (up to 100 kA) and shunts (2 kA - 180 kA)
- voltage dividers
- computerised system for data acquisition and processing [4][5]

4. Experiments on low voltage fuses

To verify the behaviour at electric arc action and thermal factor were performed tests according to IEC 60269-1 standard on low voltage fuses. According to the program below, were performed breaking tests on 500 V/250 A fuses. [6]

A) Tests program

Tests program is the following:

**Test duty 1**
- One current calibration test at Ik = 120 kA
- Three breaking tests at parameters: Ur = 550 V; I1 = 60 kA; cos φ = 0.15; ϕ = 400 - 650 (one test) and ϕ = 650 - 900 (two tests)

**Test duty 2**
- One current calibration test at Ik = 16.9 kA
- Three breaking tests at parameters: Ur = 550 V; I2 = 16,9 kA; cos φ = 0.25; ϕ = 0 - 20°

**Test duty 3**
- One current calibration test at Ik = 13.4 kA
- One breaking test at parameters: Ur = 550 V; I3 = 13.4 kA; cos φ = 0.4

**Test duty 4**
- One current calibration test at Ik = 0.90 kA
- One breaking test at parameters: Ur = 550 V; I4 = 900 A; cos φ = 0.4

**Test duty 5**
- One current calibration test at Ik = 0.50 kA
- One breaking test at parameters: Ur = 550 V; I5 = 500 A; cos φ = 0.4

B) The elements and parameters of testing circuit

The aspect of fuse in the test circuit is presented in Figure 3. [6]
C) Data obtained at tests

The results obtained by processing the oscillograms presented in Table 3. For each test duty was selected an oscillogram presented in Figures 4-8.

![Figure 3 - The aspect of fuse in the test circuit](image)

![Figure 4 – Oscillogram from test duty I₁](image)
Figure 5 – Oscillogram from test duty $I_2$.

Figure 6 – Oscillogram from test duty $I_3$. 
Figure 7 – Oscillogram from test duty I₄

Figure 8 – Oscillogram from test duty I₅
Table 3 - Main results obtained at tests on fuses

<table>
<thead>
<tr>
<th>Test No.</th>
<th>Test duty.</th>
<th>Recovery voltage</th>
<th>I_C/I_D</th>
<th>Time</th>
<th>Pre-arcing</th>
<th>Arcing</th>
<th>Total</th>
<th>Pre-arcing</th>
<th>Arcing</th>
<th>Total</th>
<th>Over-voltage</th>
<th>Making after voltage zero / Angle of arcing initiation</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>[V]</td>
<td>[kA]</td>
<td>[ms]</td>
<td>[ms]</td>
<td>[ms]</td>
<td></td>
<td>[10^x A(\cdot) s]</td>
<td>[10^x A(\cdot) s]</td>
<td>[10^x A(\cdot) s]</td>
<td>[V]</td>
<td>°</td>
</tr>
<tr>
<td>1.</td>
<td>I_1</td>
<td>550</td>
<td>26.7</td>
<td>0.85</td>
<td>4.5</td>
<td>5.35</td>
<td>198</td>
<td>273</td>
<td>471</td>
<td>1126</td>
<td>-/54</td>
<td></td>
</tr>
<tr>
<td>2.</td>
<td>I_1</td>
<td>550</td>
<td>28.1</td>
<td>0.75</td>
<td>3.9</td>
<td>4.65</td>
<td>204</td>
<td>279</td>
<td>494</td>
<td>1216</td>
<td>-/66.6</td>
<td></td>
</tr>
<tr>
<td>3.</td>
<td>I_1</td>
<td>550</td>
<td>27.9</td>
<td>0.75</td>
<td>4.1</td>
<td>4.85</td>
<td>215</td>
<td>273</td>
<td>488</td>
<td>1199</td>
<td>-/68</td>
<td></td>
</tr>
<tr>
<td>4.</td>
<td>I_2</td>
<td>550</td>
<td>14.9</td>
<td>3.5</td>
<td>4.8</td>
<td>8.3</td>
<td>211</td>
<td>310</td>
<td>521</td>
<td>1239</td>
<td>10.8/-</td>
<td></td>
</tr>
<tr>
<td>5.</td>
<td>I_2</td>
<td>550</td>
<td>15.2</td>
<td>3.7</td>
<td>4.7</td>
<td>8.4</td>
<td>225</td>
<td>356</td>
<td>581</td>
<td>1239</td>
<td>9.9/-</td>
<td></td>
</tr>
<tr>
<td>6.</td>
<td>I_2</td>
<td>550</td>
<td>15.1</td>
<td>3.7</td>
<td>4.65</td>
<td>8.35</td>
<td>223</td>
<td>355</td>
<td>578</td>
<td>1236</td>
<td>9/-</td>
<td></td>
</tr>
<tr>
<td>7.</td>
<td>I_3</td>
<td>550</td>
<td>1.34</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-/-</td>
<td></td>
</tr>
<tr>
<td>8.</td>
<td>I_4</td>
<td>550</td>
<td>0.92</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-/-</td>
<td></td>
</tr>
<tr>
<td>9.</td>
<td>I_5</td>
<td>550</td>
<td>0.6</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-/-</td>
<td></td>
</tr>
<tr>
<td>10.</td>
<td>I_Dmin</td>
<td>320</td>
<td>8.1</td>
<td>4.2</td>
<td>2.95</td>
<td>7.15</td>
<td>102.5</td>
<td>60.3</td>
<td>163</td>
<td>454</td>
<td>16.2/-</td>
<td></td>
</tr>
<tr>
<td>11.</td>
<td>I_Dmin</td>
<td>320</td>
<td>8.0</td>
<td>4.5</td>
<td>2.75</td>
<td>7.25</td>
<td>102.1</td>
<td>60.1</td>
<td>162.2</td>
<td>890</td>
<td>17.1/-</td>
<td></td>
</tr>
<tr>
<td>12.</td>
<td>I_Dmax</td>
<td>320</td>
<td>12.9</td>
<td>4.7</td>
<td>2.7</td>
<td>7.4</td>
<td>205.2</td>
<td>141.1</td>
<td>346.3</td>
<td>967</td>
<td>4.5/-</td>
<td></td>
</tr>
<tr>
<td>13.</td>
<td>I_Dmax</td>
<td>320</td>
<td>13.1</td>
<td>4.75</td>
<td>2.85</td>
<td>7.6</td>
<td>208.3</td>
<td>154.6</td>
<td>362.9</td>
<td>955</td>
<td>7.2/-</td>
<td></td>
</tr>
</tbody>
</table>

I_C – cut-off current; I_D – discrimination current; Ur – recovery voltage (was maintained 15 s at all tests)

D) Appreciation criteria for fuses behaviour at tests

According to IEC [2] there are fulfilled the following acceptance criteria:

- Fuses did not break during the tests.
- The arc voltage occurring during test of fuse in test duty I_1 and I_2 did not exceed 2500 V.
- There were not observed permanent arcing, flashovers or ejection of flames which may be dangerous for surrounding.
- The resistance between fuse contacts measured after each test was greater than 100 kΩ.
- The operating I^2t values did not exceed the values indicated by manufacturer and specified in IEC (see Table 3): 250 \cdot 10^3 \text{ A}^2\text{s} < I^2t < 760 \cdot 10^3 \text{ A}^2\text{s}
- The pre-arcing I^2t values for test duty I_2 were calculated according with IEC method.
- The discrimination of the fuse was verified by means of time-current characteristics and the pre-arcing and operating I^2t values.
- High speed acquisition and processing computerized system, which gives immediately after test current and voltage parameters and other special characteristics (like ∫i(t)²dt energy, breaking angles etc)

6. Conclusions

- The realised circuit allows the achievement of tests both for switchgears and controlgears and for low voltage fuses.
- Experiments performed on low voltage fuses of 550 V/250 A shown a good behaviour for short-circuit and overload currents passing, without long arc durations and without great arc energies
- The values of I^2t and breaking behaviour are in normal ranges, within those previewed by IEC standards.
- Overvoltages during the tests did not exceed the values stated in IEC standards.
- Isolation resistances after tests are good.

REFERENCES: