A Parallel Approach for Real-Time Power Flow in Distribution Networks

Rafael G. Milbradt*, Luciane N. Canha*, Paulo R. Pereira*, Alzenira R. Abaide*, Lukas Neusser*, Sandro R. Schmaedecke* and Lidia M. Garcia*

*UFSM – Federal University of Santa Maria
Santa Maria, RS, Brazil

#CEE-D - State Company of Electric Power Distribution of Rio Grande do Sul
Porto Alegre, RS, Brazil

E-mails: rmilbradt@gmail.com, incanha@ct.ufsm.br, paulo.ricardo@ceee.com.br, alzenira@ct.ufsm.br, lukasneusser@hotmail.com, sandroms@ceee.com.br, lidiamdg@gmail.com

Abstract

The new reality of smart distribution systems with use of generation sources of small and medium sizes brings new challenges for the operation of these systems. The complexity and the large number of nodes requires use of methods which can reduce the processing time of algorithms such as power flow, allowing its use in real time. This paper presents a known methodology for calculating the power flow in three phases using backward/forward sweep method, and also considering others network elements such as voltage regulators, shunt capacitors and sources of dispersed generation PV and PQ. After that, new elements are introduced that allow the parallelization of this algorithm and an adequate distribution of work between the available processors. The algorithm was implemented using a multi-tiered architecture which allows its use even in environments with low processing power, such as smartphones and tablets, and the processing times were measured in many network configurations and compared with the same algorithm in the serial version.

Key words


1. Introduction

The evolution of electric power systems and the requirements of the society for it, are bringing these new concepts for distribution systems such as smart metering, dispersed generation, demand-side management, microgeneration, electric vehicles, among others. In contrast, the operation of a distribution system in this way will become extremely complex, making it necessary to analyze the network more efficient, comprehensive and in real-time. It must be considered that the requirements for power quality will increase, making necessary some immediate interventions in the system to avoid the loss of the quality indicators.

The complexity of a distribution network and its large number of nodes, especially the need to consider the adjacent networks to a feeder makes the processing load for the analysis algorithms potentially large. With the introduction of remote measurement in primary distribution networks there is the possibility of real-time analysis, but the algorithms need to be optimized to a fast execution, which can be called "real time".

The traditional methods for calculating the power flow, that are widely used in transmission systems, as Newton-Raphson [1] and Fast Decoupled [2], typically do not have a good performance when applied to distribution systems. Several peculiarities of these networks such as the low ratio R/X, the unbalanced loads between phases, the presence of dispersed generation and the large number of nodes cause convergence problems to these algorithms.

According to [3] the method of backward/forward sweep is most suitable for purely radial systems, or even with a few loops, and has good reliability and fast convergence. About this same algorithm there are some variants as the method of sum of the currents [4] and the method of sum of powers [5].

Another know variant is in the calculation of the flow separated by phase [4] [5], rather than the single-phase also widely used in transmission systems. This approach is interesting because the unbalanced loads between phases are quite common in distribution networks because of the extensive presence of two-phase and single phase loads. On the other hand, the electrical utilities try to minimize the effects of these loads, distributing them symmetrically between the phases. However this symmetry is difficult to obtain considering also differences in the behavior of the loads.

This research is part of a project with CEEE-D electric utility, presenting a known methodology for the analysis of power flow in multi-phase primary and radial.
distribution networks, based in the method of backward/forward sweep (sum of the currents). In combination, are also being proposed changes to the basic algorithm for parallel processing, allowing a gain in processing time proportional to the number of available processors. The implementation of these algorithms was done in a multi-tiered architecture allowing access even in devices with low processing power and processing times were measured in various network configurations and related to the times of the basic serial algorithm.

2. Multiphase Power Flow

2.1. Basic Algorithm

For the implementation of the power flow, was chosen the backward/forward sweep algorithm with sum of the currents, adapted for the calculation in three-phase primary distribution networks. The loads represented by the distribution transformers were modeled as constant power loads (PQ). The network is computationally represented by a hierarchical data structure, where each node keeps a reference to the line section downstream and upstream, each line section also holds references to the nodes at start and end. This approach allows a better structuring of algorithms for navigation through the use of recursion. The access speed is similar to that obtained with the use of matrix/arrays and compared to these, dispensing an initial process of feeder levelization.

The algorithm is initialized with the values of nominal voltage magnitudes and angles for all of nodes and branches. Thereafter, begin k successive iterations of the following steps:

1) Calculation of currents in the PQ buses:

All PQ buses of the network are covered, calculating the currents taking into account the bus voltage (1 p.u. in the first iteration) and the admittance matrix of the shunt elements. The admittances of the shunt elements are symmetrical, as well as the mutual admittances in most distribution networks are negligible.

\[
\begin{bmatrix}
I_{a}^{(k)} \\
I_{b}^{(k)} \\
I_{c}^{(k)}
\end{bmatrix} = \begin{bmatrix}
\left(\frac{1}{S_{a}}\right)^{q(k-1)} & \frac{-Y_{a}}{S_{a}} & \frac{-Y_{w}}{S_{a}} \\
\frac{-Y_{a}}{S_{a}} & \left(\frac{1}{S_{b}}\right)^{q(k-1)} & \frac{-Y_{b}}{S_{b}} \\
\frac{-Y_{w}}{S_{a}} & \frac{-Y_{b}}{S_{b}} & \left(\frac{1}{S_{c}}\right)^{q(k-1)}
\end{bmatrix} \begin{bmatrix}
V_{a}^{(k-1)} \\
V_{b}^{(k-1)} \\
V_{w}^{(k-1)}
\end{bmatrix}
\]  

(1)

Where:

- \(I_{a}, I_{b}, I_{c}\) are the current injections at node \(i\) in the respective phases.
- \(S_{a}, S_{b}, S_{c}\) are known power injections at node \(i\).
- \(V_{a}, V_{b}, V_{w}\) are voltages at node \(i\).
- \(Y_{a}, Y_{b}, Y_{w}\) are admittances of the shunt elements at node \(i\).

2) Backward Sweep:

Based on Kirchhoff’s Law, this step consists to go through all nodes from the loads toward the source by adding the currents.

\[
\begin{bmatrix}
J_{la}^{(k)} \\
J_{lb}^{(k)} \\
J_{lc}^{(k)}
\end{bmatrix} = -\begin{bmatrix}
I_{ja}^{(k)} \\
I_{jb}^{(k)} \\
I_{jc}^{(k)}
\end{bmatrix} + \sum_{m \in M} \begin{bmatrix} J_{ma}^{(k)} \\ J_{mb}^{(k)} \\ J_{mc}^{(k)} \end{bmatrix}
\]  

(2)

Where:

- \(J_{la}, J_{lb}, J_{lc}\) are the per phase currents at branch \(l\).
- \(M\) is the set of branches downstream of \(l\).

3) Forward Sweep:

It consists in the calculation of the voltage magnitude and angle, for each node in the forward direction starting from the substation. The voltage values will be used to calculate the new currents in Step 1, in the next iteration of this algorithm.

\[
\begin{bmatrix}
V_{ja}^{(k)} \\
V_{jb}^{(k)} \\
V_{jc}^{(k)}
\end{bmatrix} = \begin{bmatrix}
V_{ia}^{(k)} \\
V_{ib}^{(k)} \\
V_{ic}^{(k)}
\end{bmatrix} - \begin{bmatrix}
z_{aa} & z_{ab} & z_{ac} \\
z_{ba} & z_{bb} & z_{bc} \\
z_{ca} & z_{cb} & z_{cc}
\end{bmatrix} \begin{bmatrix}
J_{la}^{(k)} \\
J_{lb}^{(k)} \\
J_{lc}^{(k)}
\end{bmatrix}
\]  

(3)

4) Convergence Checking

In this step is made the convergence test with the results, which is based in the comparison of voltage value at each phase for each node. If the difference in any of these voltage values between the actual iteration and the previous iteration is less than the criterion to stop, then the power flow has reached the convergence.

\[
\begin{bmatrix}
\Delta V_{ia}^{(k)} \\
\Delta V_{ib}^{(k)} \\
\Delta V_{ic}^{(k)}
\end{bmatrix} = \begin{bmatrix}
V_{ia}^{(k)} \\
V_{ib}^{(k)} \\
V_{ic}^{(k)}
\end{bmatrix} - \begin{bmatrix}
V_{ia}^{(k-1)} \\
V_{ib}^{(k-1)} \\
V_{ic}^{(k-1)}
\end{bmatrix}
\]  

(4)

2.2. Dispersed Generation

The modeling of this equipment in a power flow algorithm is a required task. Especially the sources of distributed generation in recent years have grown in number considerably. However, this growth has become a challenge for the operation of the distribution system and the proper modeling and analysis of the network through the power flow in real time can be an important ally.

The sources of dispersed generation can be modeled in the following ways:

1) PQ Buses: buses where the active and reactive power are constants;
2) PV Buses: buses where the active power and voltage are constants.
PQ buses are easy to implement in the Power Flow and requires no further treatment. They are inserted in steps of calculation as power injections, with the opposite sign of loads.

The PV buses have constant values of voltage and real power, since the reactive power can vary between a minimum and maximum. It is necessary a new step in every iteration of the algorithm, this is made by calculating the required reactive power injection in the network so the voltage at that node stay equal to the voltage set to this PV bus [6][7]. As the voltage at node changes with each sweep of the algorithm, the injection of reactive power will also changes, therefore this criterion also becomes part of the step where the convergence is checked.

2.3. Voltage Regulators and Shunt Capacitors

The voltage regulators are widely used in Brazilian networks, which often are long and including urban and rural areas within the same feeder. These regulators usually have variable tap which is defined by Power Flow algorithm through these parameters and characteristics of the regulator control: PT ratio (potential transformer), UR and UX (resistance and reactance to the load center, in the case of regulators with line drop compensation), number of available tap’s, connection type and the reference voltage. The voltage regulator output is calculated during the power flow algorithm considering all of these parameters plus the instant values of node voltage and current.

This is an approximate method for predicting the current regulator tap. It is known that the tap switching causes an equipment wear, this effect makes necessary to take other actions to prevent an excessive number of switching as the establishment of insensitivity levels and time delay. The prediction of current tap, because of these features becomes more difficult, so for real time application of the power flow the better case is to read this parameter remotely via a SCADA system, which needs to be integrated into the Power Flow algorithm.

Shunt capacitors are also used for voltage control through reactive compensation and has important influence on the Power Flow. These are modeled as reactive power injections of fixed value and can be connected or not to the network, depending on the current value in that node or the time of day, in the case of having an automatic control module.

2.4. Meshed Networks

The networks that have loops cannot have the Power Flow algorithm based on backward/forward sweep applied directly. Under this condition the algorithm should enter into an infinite recursion, causing a stack overflow.

In these cases the network must go through an initial phase where breakpoints are inserted in order to transform the meshed network into an equivalent radial network. At the breaking point is inserted a bus with a current injection calculated by the method of impedance compensation at the breaking point which is discussed in [4] [8].

3. Parallel Power Flow

3.1. Parallel Computing

The evolution of microprocessors few years ago, has encountered a limit on clock frequency which keep them from work above a certain frequency range. Therefore the evolution of processing capability is giving by optimizing the circuits and multiprocessing, which is to include more than one processor on a single chip. The multiprocessing capability is spreading among the processors for all types of applications and software developed can use this feature reducing its running time through parallel programming.

The proposed methodology for the parallelization of the power flow algorithm can be applied to any other algorithm of forward/backward sweep into a radial network.

3.2. Implementation Principles

An algorithm based on backward/forward sweep can operate in a general way with two main functions:

1) Prefixed Action: is the action that is performed on each node during the step of forward sweeping. Should accept parameters, which are processed and transmitted for processing at the downstream nodes.

2) Postfixed Action: is the action that is performed on each node during the backward sweep. The processing should be performed on the node and the values of interest should be returned to the upstream nodes. As a node can have multiple branches, should also be provided an algorithm to operate over the values of the branches, before returning to the parent node.

The separation of these two elements allows the implementation of the parallelism independently of the algorithm implementation (which is basically composed by prefixed and postfixed actions). The basic principle of the parallel implementation is that from the trunk path of the feeder, all the ramifications can be computed in parallel.

This implies that the feeder can be broke into many feeders and the Power Flow can be solve for that feeders in a parallel way. The Power Flow also must be calculated into the main path and this calculation is responsible by the synchronization between ramifications.

In the first step the algorithm should find the main path of the feeder. This can be determined as the path from the substation to a leaf node (in the end of the network) with the greatest number of hops (Figure 1). Another task of preparation is create slave workers, who will be responsible for processing actions (prefixed and postfixed). The number of slaves created will be equal to the number of the system available processors minus 1.
The master worker is the agent responsible for coordinating all the actions of slaves, including the ramifications to be processed. This is made by sending voltage values to the ramifications in the forward sweep and receiving the current amount while in the backward sweep.

The algorithm of the master worker consists of forward sweeping in the main path of the feeder. When it finds a ramification, that ramification will be transferred to a free slave with the needed parameters (voltage) for power flow calculations. The master worker keeps its forward sweep and when it reaches the last node of the trunk, starts to backward sweep, then the master will operate on the results of branches (sum of the currents).

Therefore, during the stage of discovering the main path, the algorithm compares the number of nodes of this path relative to the number of nodes in the feeder and the number of workers. With the goal of find a balance between the work of master and slaves, the end of the main path can be transformed into a new branch, which cease to be part of the that path and will be processed by one of the slaves.

This approach allows the algorithm to adapt regardless of the topological characteristics of each network, in order to obtain an adaptive balancing and trying to get the most processing power from the available processors.

4. Results

4.1. Simulations

The final algorithm was tested using real distribution networks of the CEEE-D, located in southern Brazil. The feeders had 396, 679, and 2517 nodes and another variable in the tests was also the number of available processors that were 2, 4 and 6.

To demonstrate the performance improvement obtained from the parallel algorithm, the power flow was executed on the testing networks in serial and parallel version. The performance values showed in the Figure 3 are the execution time relation between the parallel and the serial implementation. It is possible to realize that the parallelism becomes clearly more efficient as the network nodes were increased. This feature is quite common in Brazilian distribution networks outside of the major urban centers. The algorithm, in the tests performed always was faster than the original, and due to load balancing obtained a performance which could be a linear function of the number of processors.

![Figure 3: Ratio between the runtime of the serial and parallel algorithm according to the number of nodes and processors.](image)

4.2. Software Tool

The algorithms were developed in Java language using a multi-tiered model, where the server tier, which is effectively responsible for processing the Power Flow calculation can be hosted on a powerful multi-processor computer. The client tier technology was developed using Google Web Toolkit ® (Figure 4), which allows visualization of the topology and operation of the

![Figure 2: Information flow between the actors of parallel power flow.](image)
Software in any Internet browser, or even from smartphones and tablets.

In addition to the real-time characteristic, which could be obtained through the parallel algorithm, ubiquity is also a desirable feature since the popularization of devices such as smartphones and tablets. This multi-tiered model allows that the availability on many devices does not adversely affect the runtime of the application.

Using the methods discussed in [6] [7] was also possible to model the sources of distributed generation in primary distribution networks. Finally, the modeling of multi-tier application brought great advantages as the possibility of real-time execution of complex algorithms even in devices with low processing power.

**Acknowledgments**

The authors would like to thank PPGEE, UFSM, CAPES and CEEE-D for the financial support provided to this project and also “ANEEL – Agência Nacional de Energia Elétrica” through the program of R & D - Research and Development.

**References**


**5. Conclusions**

The presented paper proposes the use of a parallel algorithm in order to implementing the Power Flow in real time even in distribution networks with large numbers of nodes. The reduced time execution will allow the integration of these algorithms to remote measurements along the distribution network as well as measurements of loads on the smart metering devices. The proposed methodology for parallelization was applied to the classic backward/forward sweep power flow algorithm, and also can have execution times even lower if applied to some recent improvements proposed in [8].