Optimal Placement and Sizing of DG for Loss Reduction, Voltage Profile Improvement and Voltage Sag Mitigation

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Abstract. Increasing trend in using distributed generation has motivated power sector planners to consider approaches that result in benefiting the most from investments in this field. Meanwhile subjects such as power loss reduction, voltage profile improvement and voltage sag mitigation has significant role in lessoning imposed expenditures to utility companies. Loss reduction is important for utility companies as it directly increases company benefit in a competitive electricity market, beside reaching power quality standards is too important as it has vital effect on customer orientation. In this paper an optimization algorithm is developed to gain these goals all together. In order to evaluate sag mitigation capability of the proposed algorithm, voltage in voltage sensitive buses is investigated. An existing 20KV network has been chosen as test network. Results show the efficiency of the proposed algorithm in reducing power loss, improving voltage profile and mitigating voltage sags in voltage sensitive buses.

Key words
power loss, voltage profile, voltage sag mitigation, sensitive loads

1. Introduction

In a general concept DG (Distributed Generation) is power generation near consumption. Reducing distance between generation centers and consumption colonies has numerous. Literature overview on this subject shows that using DG units in a feeder reduces power loss significantly, even if DG is not supplying a large percentage of the load. The percentage of loss reduction can reach 85% in different load patterns [1]. On the other hand current flow limitation in long branches lessons the need for reactive power and thus voltage profile is improved. This characteristic, especially in long feeders which suffer from poor voltage profile is an important motivation to benefit DG. Beside growing use of voltage sensitive equipments, growing awareness about "undistorted power in PCC" \(^1\) and need for uninterruptible and high quality power to survive in competitive economic markets for commercial and industrial consumers are just a few reasons for the importance of power quality. Considering these facts and high DG penetration level in future grids, using all potential capacities for gaining the most from DG technologies seem evitable. Considering the fact that conventional distribution grids are commonly fed unidirectional, a fault or starting a large size motor in one bus of the grid can cause voltage sag in buses in vicinity [2]. DG supports voltage in connection point [3] and this efficiency is highly dependant on the size and location of DG unit, so optimal sizing and location of DG can give the opportunity to benefit this potential capacity to mitigate voltage sags, particularly in buses with sensitive loads.

In literature valuable studies have been done on assessment the impact of using DG on the power quality of distribution networks. In [4] different sag related indices have been developed and impact of DG penetration levels has been investigated on defined indices. However it should be noted that in network expansion programming, other network operation concerns such as loss minimization and improving voltage profile in different loadings are also important factors. In this study a comprehensive approach is followed.

2. Genetic Algorithm

Genetic algorithm is a computational model that relies on concepts of evolutionary processes [5]. The genetic algorithms can be applied in optimization fields. GA\(^2\) operates on a population of possible answers (individuals or chromosomes), applying selection and reproduction criteria and by then new solutions (offspring) are generated containing information from

\(^1\) - Point of Common Coupling
\(^2\) - Genetic Algorithm
where they were originally created (parents). Obviously, the better the solution, there is a higher probability one goes to the next offspring.

The first step is encoding a potential solution in a data structure type. Once the initial population is randomly generated every solution is evaluated by means of a goal (or objective) function. In each generation the fitness value of all individuals are calculated and those with higher fitness values have more probability to be parents of next generation. At this point, some individuals are coupled by means of a crossover operator.

In continue the individuals can undergo mutation, which involves selecting a gene in a string and change it to another possible value. The probability of mutation to happen is rather small.

### 3. Problem Formulation

In this section objective functions that GA uses for optimal sizing and placement for DG units is proposed. Each of these goal functions can be used separately to follow related goal.

#### A. Power Loss Reduction

The total active power loss \( P_{Loss} \) in a distribution grid can be calculated as [6]:

\[
P_{Loss} = \sum_{j=1}^{N-1} I_j^2 R_j
\]

Where \( I_j \) is the magnitude of current for feeder section j. \( R_j \) is the resistance of feeder j and \( N-1 \) is the total number of feeder sections, in case there are \( N \) nodes in the studied grid. Loss reduction can be achieved by following fitness function:

\[
f_{Loss} = \frac{1}{P_{Loss}} \tag{2}
\]

#### B. Voltage Profile Improvement

Voltage profile improvement fitness function can be explained as (3).

\[
f_v = \frac{1}{N} \sqrt{\frac{1}{i=1} N \sum_{j=1}^{N-1} (V_n - V_j)^2} \tag{3}
\]

In which \( V_n \) is voltage level, \( V_j \) is voltage of the \( j^{th} \) bus and \( N \) is the number of buses of the network under study.

#### C. Voltage Support in Voltage Sensitive Buses

There are two different approaches to define an objective function to maximize voltage support in buses that feed voltage sensitive loads. In the first approach the focus is on the magnitude of voltage in sensitive buses during a voltage sag in eventful buses (buses with a high sag frequency index). With this approach the related objective function can be defined as below:

\[
f_{\text{Sensitive}} = \frac{1}{\sum_{i=1}^{N} K_i (V_n - V_{i, \text{Sensitive}})} \tag{4}
\]

In which \( V_n \) is voltage level of network, \( V_{i, \text{Sensitive}} \) is the voltage of \( i^{th} \) sensitive bus in the studied network and \( K_i \) is the weighting coefficient for the \( i^{th} \) voltage sensitive bus and is defined as below:

- For \( 0.1 PU \leq V_{\text{Sensitive}} \leq 0.4 PU \), \( K = 0.7 \)
- For \( 0.4 PU < V_{\text{Sensitive}} \leq 0.7 PU \), \( K = 0.2 \)
- For \( 0.7 PU < V_{\text{Sensitive}} \leq 0.9 PU \), \( K = 0.1 \)

It means that in a sensitive bus, higher voltage amplitude (during voltage sags in eventful buses) has a higher fitness value and thus proposed algorithm tends to introduce place and size of DG, which lead to this voltage condition. The main hint for choosing pre said limitation for voltage of sensitive buses \( (V_{\text{Sensitive}}) \) is given from IEC 61000-4-11 which recommends levels of 10%, 40% and 70% as the magnitude values for testing equipment against voltage sags and short interruptions. In [7] this criterion is used to form a objective function for sag mitigation by Flexible AC Transmission System (FACTS) devices.

The second approach deals with voltage sag propagation mitigation. This takes into account the number of buses that experience voltage sag. (5) Shows the proposed function to cover this concept.

\[
f_{SP} = \omega_1 V_L + \omega_2 V_M + \omega_3 V_H \tag{5}
\]

In which \( V_L \) is the number of buses with voltage amplitude drop below 0.1 p.u, \( V_M \) is the number of buses with voltage amplitude between 0.1 p.u and 0.7 p.u and finally \( V_H \) is the number of buses with voltage amplitude between 0.7 p.u and 0.9 p.u during voltage sag occurrence. Weighting coefficients are defined as \( \omega_1 = 0.1, \omega_2 = 0.3 \) and \( \omega_3 = 0.6 \) to magnify voltage support in more ill-conditioned buses. In this paper the first approach is used in simulations.

### 4. Illustrative Example

#### A. Test Network

An existing 20KV network which is modeled with 32 buses is studied. This medium voltage feeder which is located in south Khorasan province in Iran has severe voltage problems especially in peak load hours and in end feeder areas. Figure 2 shows single line diagram of this network.

In summer the voltage drops to 0.85 per unit in peak load hours. Utility company in the region has tried to solve the problem with using capacitors but the problem still exists.
Fig. 1 Single Line Diagram of the Test Network

B. $Y_{bus}$ Based Load Flow for Distribution Grids

Due to high $R/X$ ratio of the sample distribution network which is the result of being long and old, Newton-Raphson and FDLF\(^3\) load flow algorithms do not lead to reliable answers. On the other hand in proposed algorithm GA calls load flow program several times. Therefore using usual load flow algorithms makes the overall proposed algorithm architecture and runtime increases immethodical. On the other hand for modeling voltage sag we need impedance to be connected from eventful bus to ground. So we have to use a $Y_{bus}$ based load flow program. In this study proposed algorithm in [8] is used. This admittance based load flow program gives precise answers in minimum runtime. Assuming that bus $1$ as slack, in bus $i$ we have [7]:

$$V_i^* = \sum_{j=1}^{N} Y_{ij} V_j = P_i^{sp} - jQ_i^{sp}, \; i = 1, 2, ..., N, \; i \neq s$$ (6)

Where $s$ denotes the slack bus

With an appropriate solution guess

$$V_0 = [V_{10}, V_{20}, ..., V_{N0}]$$

The above equation can be written as

$$(V_{i0}^* + \Delta V_i^*) \sum_{j=1}^{N} Y_{ij} (V_{j0} + \Delta V_j) = P_i^{sp} - jQ_i^{sp}$$ (7)

By neglecting the second order terms, the following liberalized equation is obtained

$$V_{i0}^* \sum_{j=1}^{N} Y_{ij} \Delta V_j + (\sum_{j=1}^{N} Y_{ij} \Delta V_j) \Delta V_i^* \equiv$$

$$(P_i^{sp} - Q_i^{sp}) - V_{i0}^* \sum_{j=1}^{N} Y_{ij} \Delta V_j$$ (8)

In the first sigma on the left $j$ can not be qual to $s$. From Equation (6), we can obtain the following approximated relation.

$$\sum_{j=1}^{N} Y_{ij} V_{j0} \equiv (P_i^{sp} - jQ_i^{sp}) / V_{i0}^*$$ (9)

Substituting (9) into (8) and dividing both sides by $V_i^*$, yield

$$\sum_{j=1}^{N} Y_{ij} \Delta V_j + \frac{P_i^{sp} - jQ_i^{sp}}{(V_{i0}^*)^2} \Delta V_i^* \equiv \Delta I, \; i = 1, 2, ..., N, \; i \neq s$$ (10)

In the above equation, the current mismatch $\Delta I^{sp}$ is determined by

$$\Delta I_i^{sp} = \frac{(P_i^{sp} - jQ_i^{sp})}{V_{i0}^*} - \sum_{j=1}^{N} Y_{ij} V_{j0} = \frac{(P_i^{sp} - jQ_i^{sp}) - (P_i - jQ_i)}{V_{i0}^*} = \frac{\Delta P_i - j\Delta Q_i}{V_{i0}^*}$$ (11)

Where

$$\Delta P_i = P_i^{sp} - P_i$$

$$\Delta Q_i = Q_i^{sp} - Q_i$$

The above equation can be written in a matrix forms as follows:

$$Y_{bus} \Delta V + D \Delta V^* = \Delta I$$ (12)

Where

$$\Delta V = [\Delta V_1, \Delta V_2, ..., \Delta V_N]$$

$$\Delta I = [\Delta I_1, \Delta I_2, ..., \Delta I_N]$$

$$D = \text{Diag}[(P_i^{sp} - jQ_i^{sp})/V_{i0}^*]$$

As a static load model [9] has been used in this study, P-Q buses are specified and an initial voltage $V_i$ is assigned to each bus. $V_i$ is a complex value, and only N complex divisions are required in calculating diagonal matrix $D$. Expanding equation (12), it is shown in [7] that only diagonal matrix $D$ and current mismatch $\Delta I$ are required to be updated in the iteration procedure. As a result computation time reduces roughly to 40% and 50% compared with the FDLF and the NR\(^3\) methods respectively. In this case reliable convergence characteristic and precise answer is honorable.

C. Genetic Algorithm Implementation

The first step for using GA is coding the search space. In this step a string is assigned for each member of search space.

In this case there is a network with 32 buses and it is assumed three sizes of DG units are available. These sizes are 300, 600 and 900KVA. Ten percent of the capacity of each bus is dedicated to reactive power generation. Each bus is assigned with one gene in which, zero means that there is no DG in the bus, 1 resembles a 300KVA DG unit, 2 resembles 600KVA DG unit and 3 resembles 900KVA DG unit. Next step is making first chromosome (potential answers) is determined.

In the above equation, the current mismatch $\Delta I^{sp}$ is determined by

$$\Delta I_i^{sp} = \frac{(P_i^{sp} - jQ_i^{sp})}{V_{i0}^*} - \sum_{j=1}^{N} Y_{ij} V_{j0} = \frac{(P_i^{sp} - jQ_i^{sp}) - (P_i - jQ_i)}{V_{i0}^*} = \frac{\Delta P_i - j\Delta Q_i}{V_{i0}^*}$$ (11)

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$$\sum_{j=1}^{N} Y_{ij} \Delta V_j + \frac{P_i^{sp} - jQ_i^{sp}}{(V_{i0}^*)^2} \Delta V_i^* \equiv \Delta I, \; i = 1, 2, ..., N, \; i \neq s$$ (10)

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c. Implementing mutation operator.  
d. Two members of new generation are saved and treated as parents in the next generation.  
e. Best answer goes to the next generation.  
a to e procedures should be repeated to generate a limited number of individuals (200 individuals). In each step the condition for not exceeding 1500KVA is checked. Finally the member in the last generation with the best fitness value is the optimal answer to the optimization problem.

D. Simulation Results

1) Loss Reduction: In this section GA only calls power loss objective function. The optimization algorithm suggests installing DG units in 9, 15, 16 and 25 buses. It is observed that when loss minimization goal is followed the algorithm suggests the lowest available size of DG units near highly loaded buses to prevent flowing current in long lines. Below diagram shows a reduction of 63% in the network loss.

![Fig. 2 Loss Reduction in Test Network](https://doi.org/10.24084/repqj08.616)

2) Voltage Profile Improvement: In this section GA calls the voltage profile improvement fitness function. Proposed algorithm suggests installing a 900KVA DG unit in bus 16 and a 600KVA DG unit in bus 19. It is observed that optimization algorithm proposes installing DG units in end feeder buses with poor voltage profile characteristics. Paying attention to the network configuration, it is observed that two main branches are connected to the slack bus (upper main branch, called U branch and lower main branch, called L branch in this case study). Due to this special network configuration installing DG units in each of the two main branches has no effect on the voltage profile of the other main branch. As voltage profile is extremely low in the end feeder buses of U branch, proposed algorithm suggests installing DG units in these buses and the voltage profile in L branch makes no difference in this case. Figure 3 shows this behavior.

![Fig. 3 Voltage Profile Improvement in Test Network](https://doi.org/10.24084/repqj08.616)

3) Voltage Support in Voltage-Sensitive buses: If only voltage support in sensitive buses and mitigating voltage sags is the goal of optimization problem, genetic algorithm uses objective functions (4) and (5) depending on desired approach. (4) is used when voltage support in voltage sensitive buses is desired and (5) is used when reducing number of buses which experience deep voltage sags is desired. Figures 4 and 5 show voltage support in two sensitive buses of test network (bus 13 and 24) during voltage sag occurrence in eventful buses (9, 12, 23 and 29). Eventful buses have the highest annual fault rate. Here, desired approach is voltages support in sensitive buses.

![Fig. 4 Voltage in Bus 13 during Voltage Sag Occurrence in Eventful Buses](https://doi.org/10.24084/repqj08.616)

4) Comprehensive objective function: In this section all three mentioned functions are gathered to form one single objective function which follows all three desired goals.
After normalizing each of discussed functions, comprehensive objective function is:

\[ f = K_1 f_{\text{loss}} + K_2 f_v + K_3 f_{\text{sag}} \]  

(13)

In order to magnify the role of each goal, related weighting coefficient \( K_1, K_2, \) and \( K_3 \) can become enlarged. For \( K_1 = K_2 = K_3 = 1 \), table I shows proposed location and size of DG units.

<table>
<thead>
<tr>
<th>Suggested Bus</th>
<th>DG Size (KVA)</th>
</tr>
</thead>
<tbody>
<tr>
<td>16</td>
<td>300</td>
</tr>
<tr>
<td>18</td>
<td>600</td>
</tr>
<tr>
<td>25</td>
<td>600</td>
</tr>
</tbody>
</table>

Power loss reduces from 518 kw to 193 kw. Figure 5 shows voltage profile improvement in the test network.

Loss reduction, improvement in voltage profile and voltage support in sensitive buses show the acceptable efficiency of proposed algorithm.

5. Conclusion

Due to high employment of voltage sensitive equipments, voltage support in sensitive loads are a great concern for utility companies. Beside network operation cost is directly affected by power loss. In this paper a long and highly loaded 20KV feeder has been under investigation to find optimal location and sizing for DG units to support 30% of the feeder load. This DG penetration level is reasonable due to economic considerations. The presence of two highly voltage sensitive loads in this feeder have made network expansion planners think of voltage sag mitigation and voltage support in these buses. Beside, because of ill-conditioned network, Loss reduction and voltage profile improvement have been of great concern as well. Objective functions with different approaches have been defined and different scenarios were investigated. Results show significant reduction in power loss in addition to voltage profile improvement. Capability of proposed method for voltage sag mitigation in sensitive buses has been also investigated and was proved to be within acceptable limits.

References